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FIG.1A

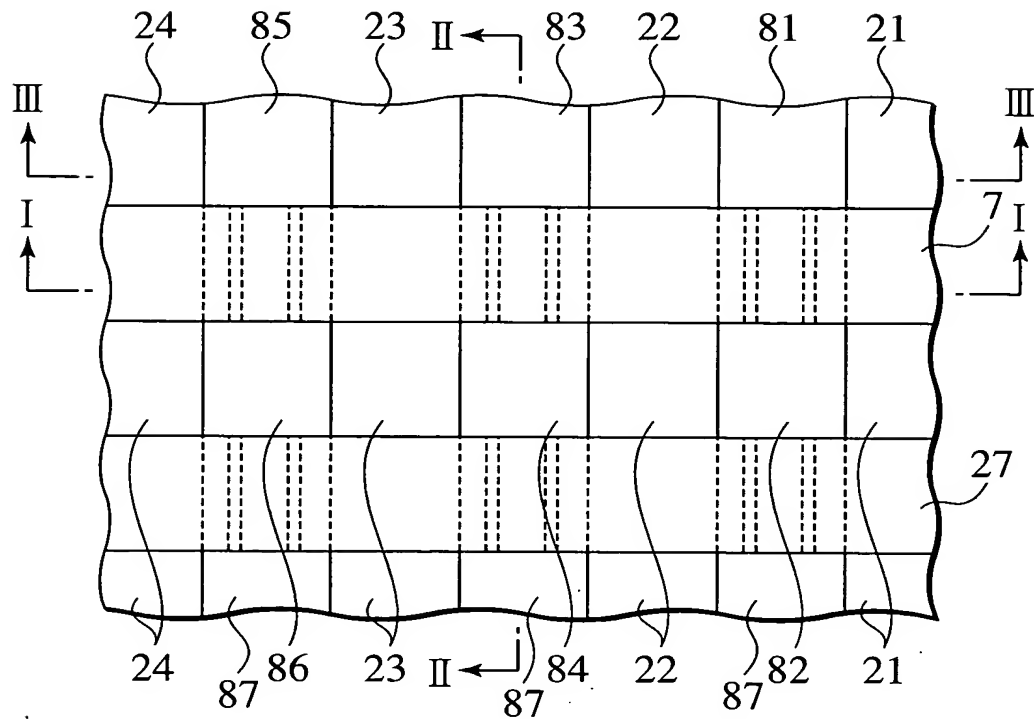
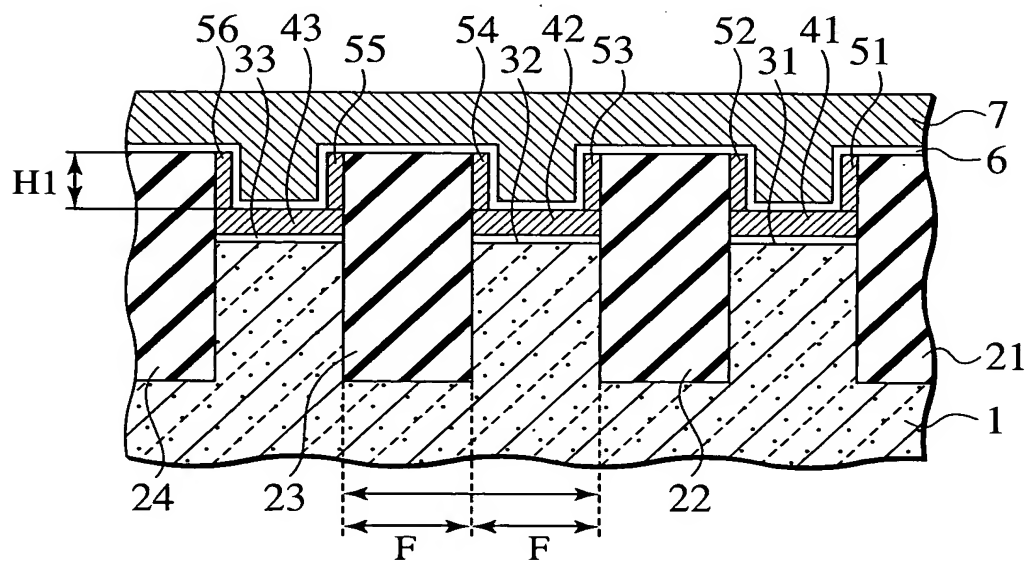


FIG.1B



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FIG.2A

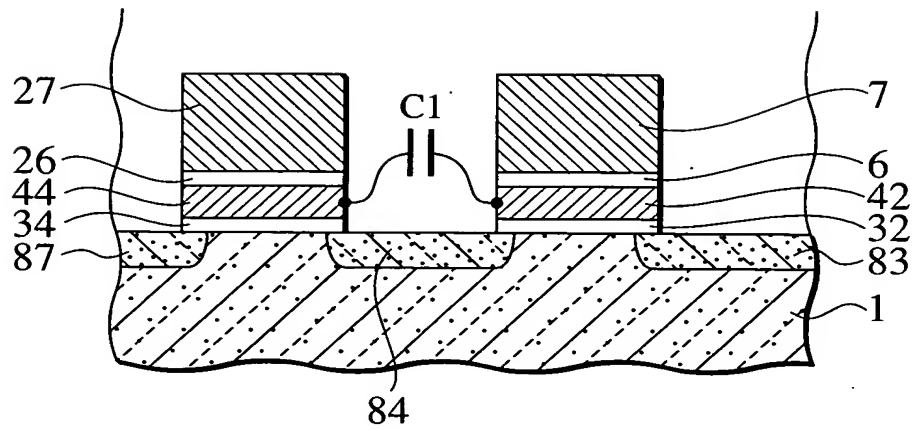
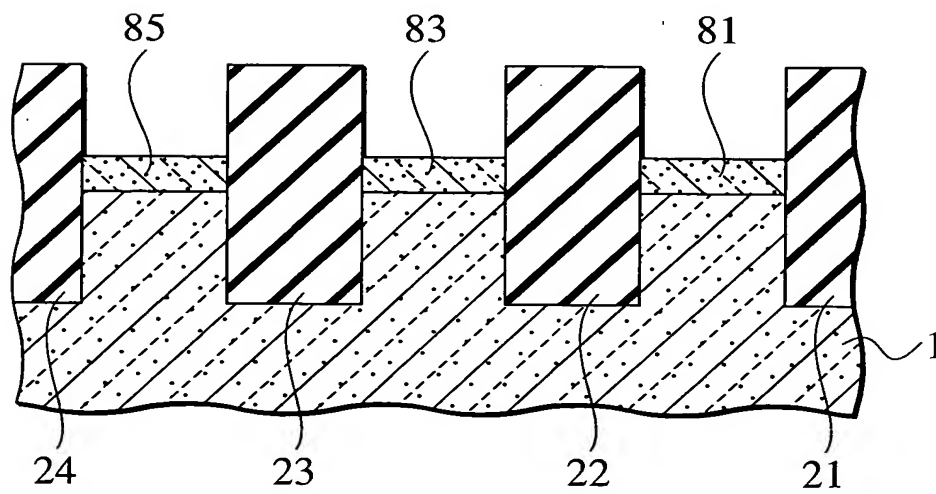


FIG.2B



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FIG.3A

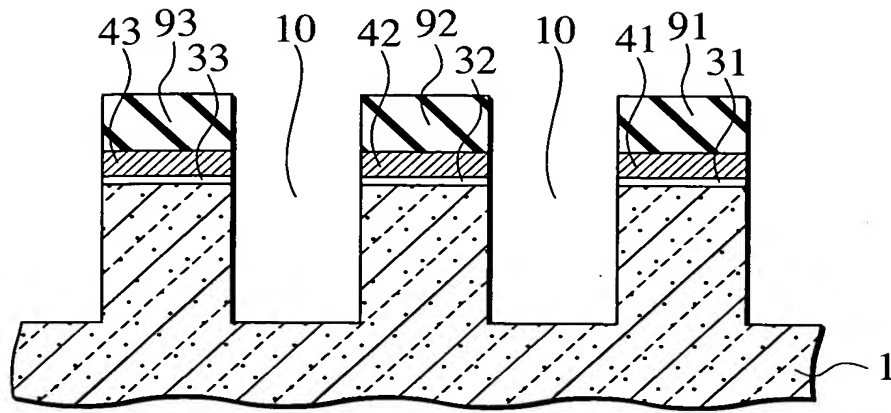


FIG.3B

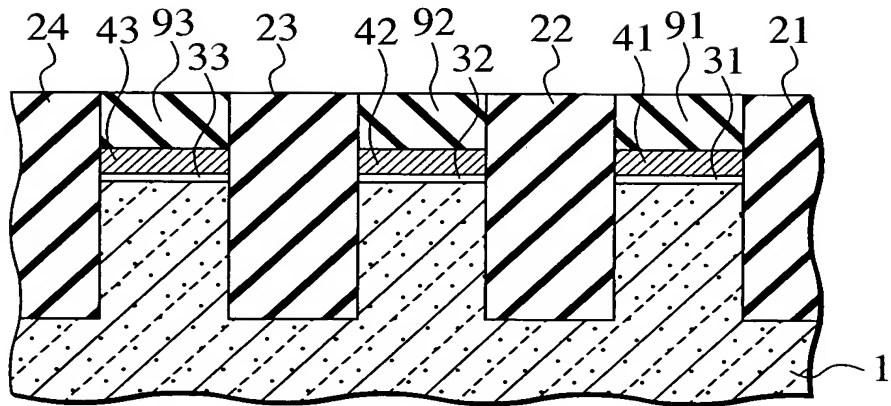
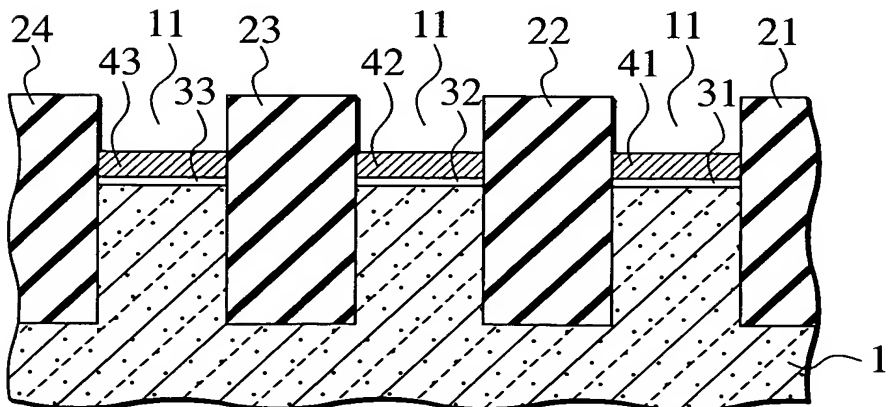


FIG.3C



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FIG.4A

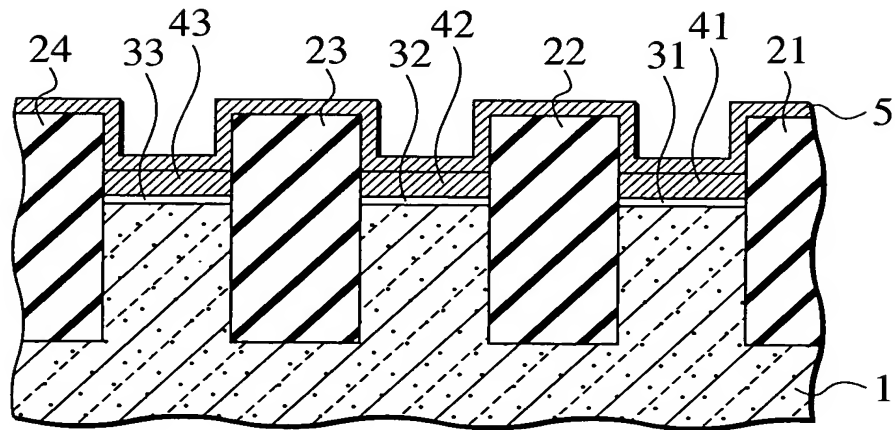


FIG.4B

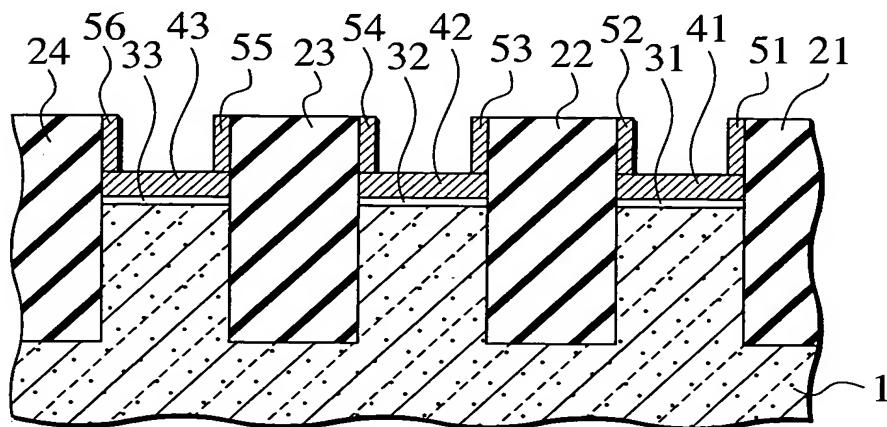


FIG.4C

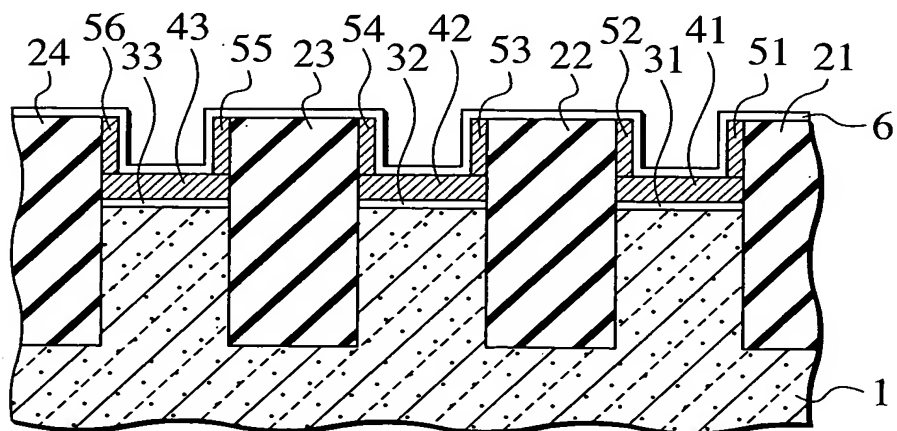
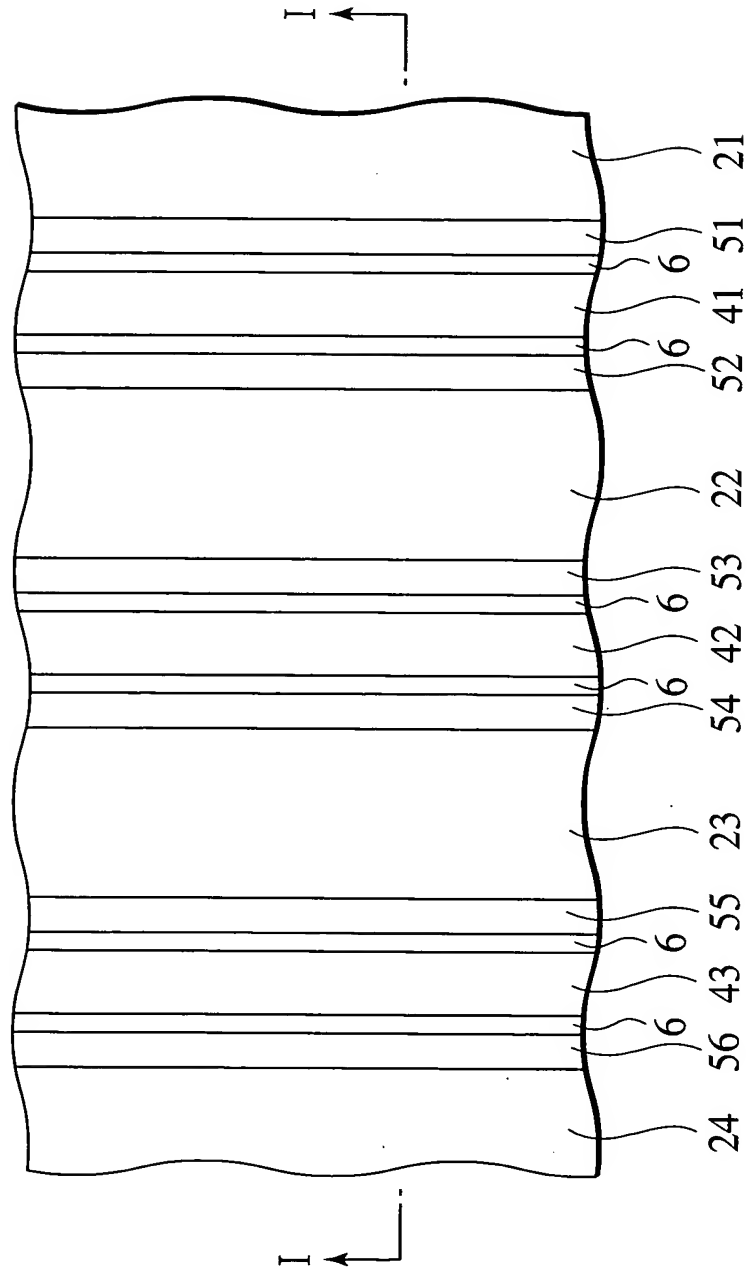


FIG. 5



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FIG.6

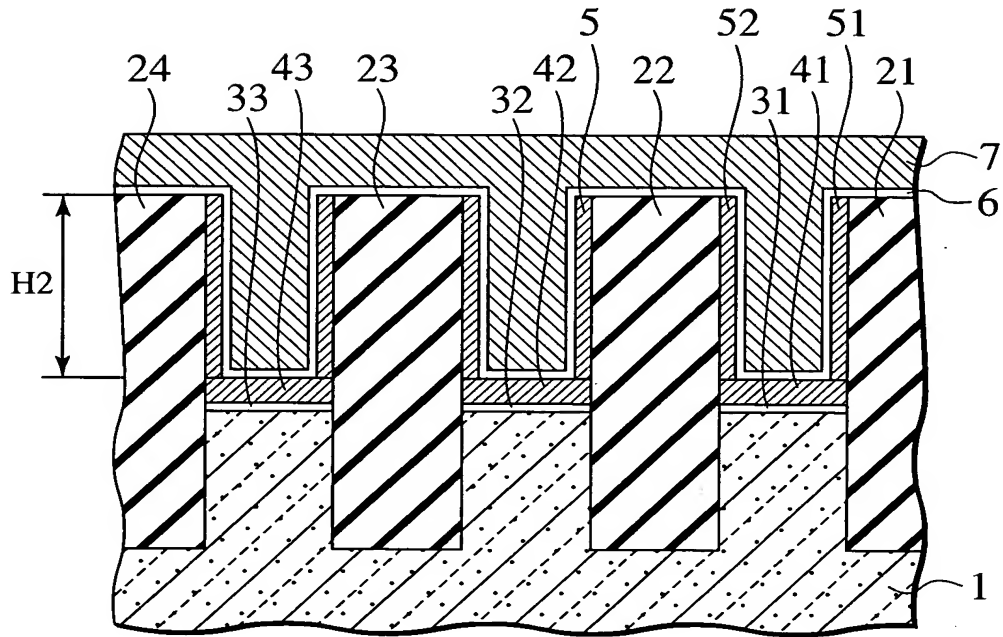
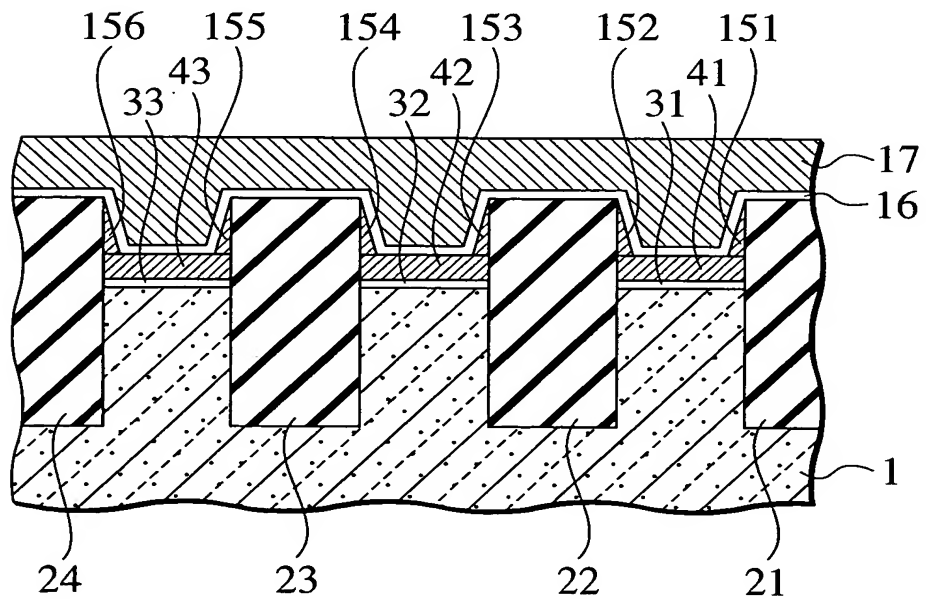


FIG.7



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FIG.8A

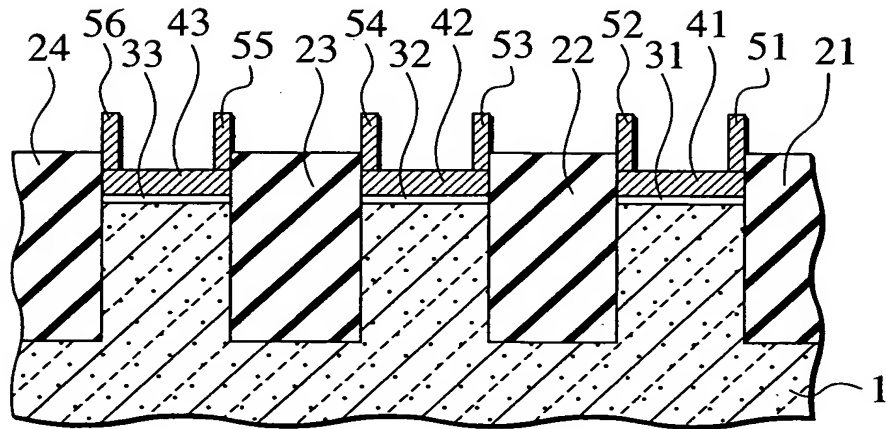
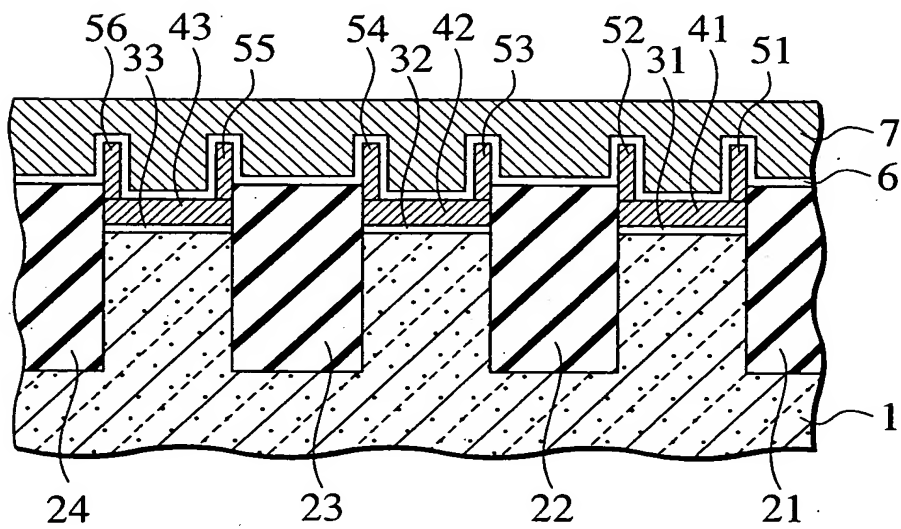


FIG.8B



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FIG.9A

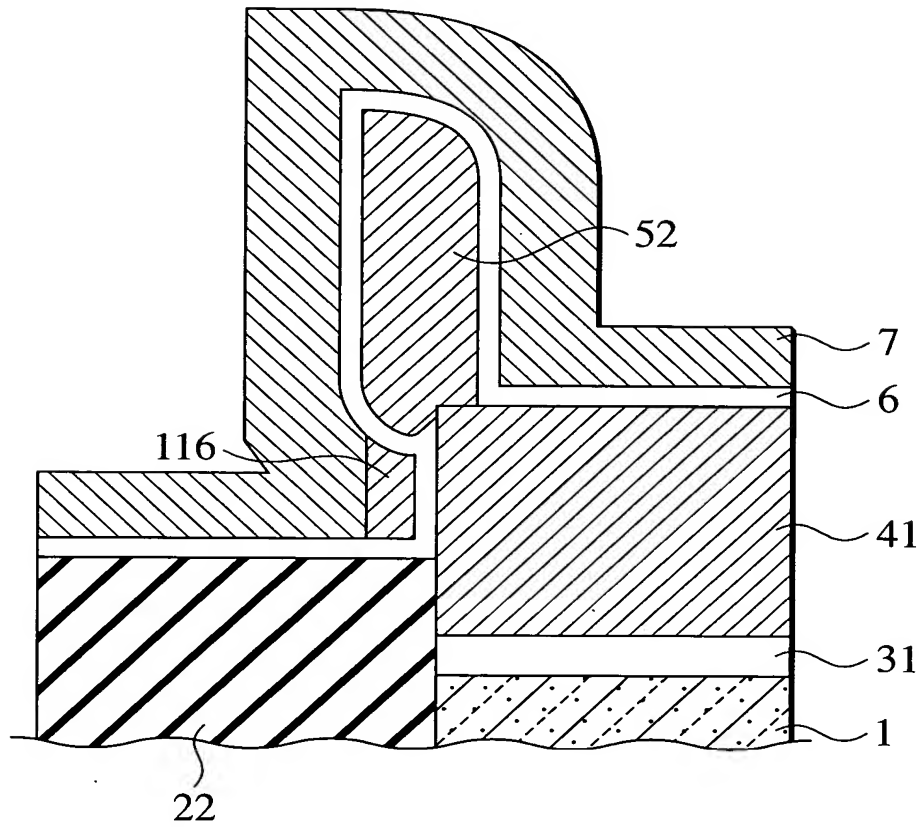
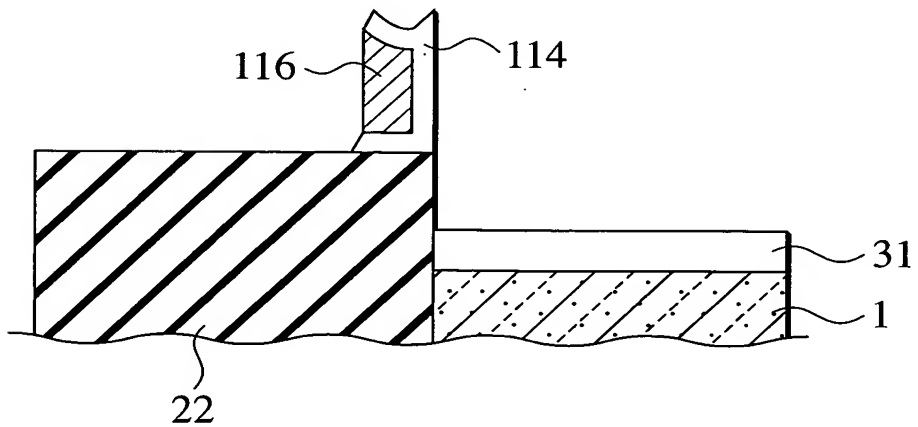


FIG.9B







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FIG.11A

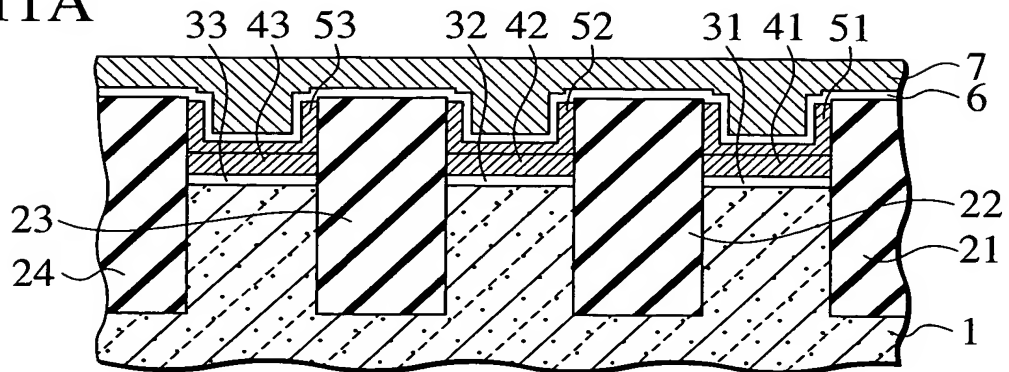


FIG.11B

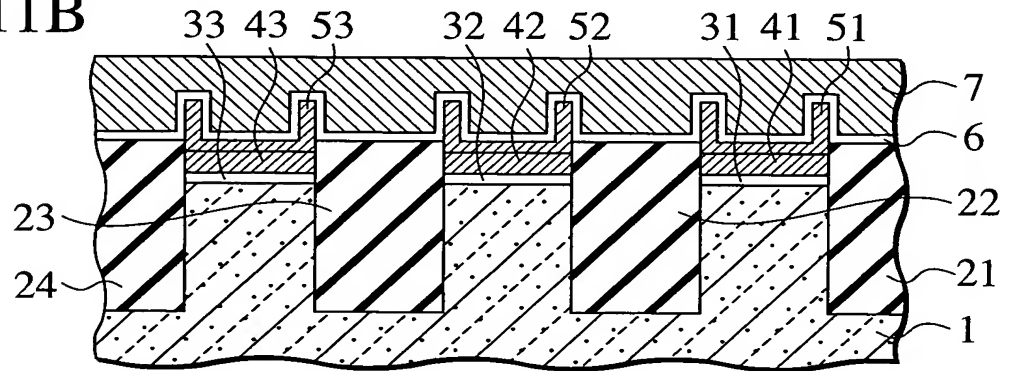


FIG.11C

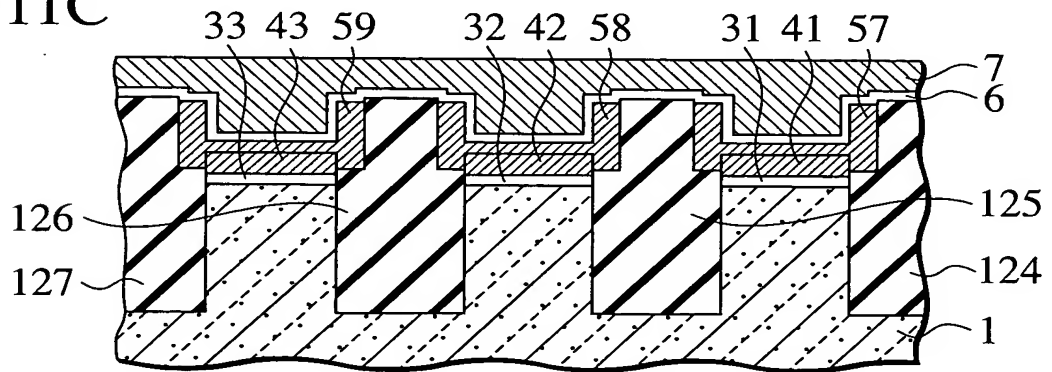


FIG.11D

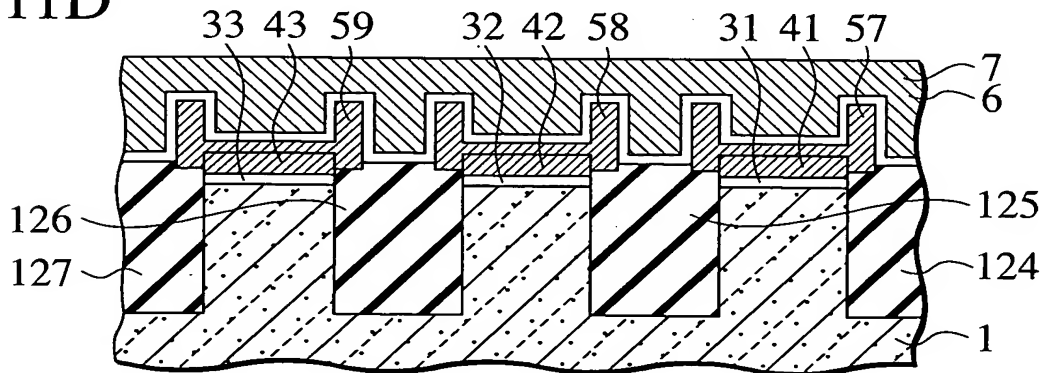


FIG.12A

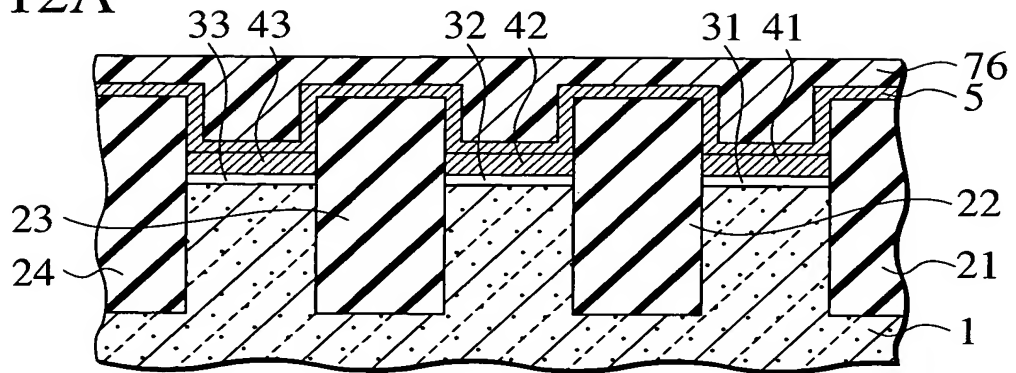


FIG.12B

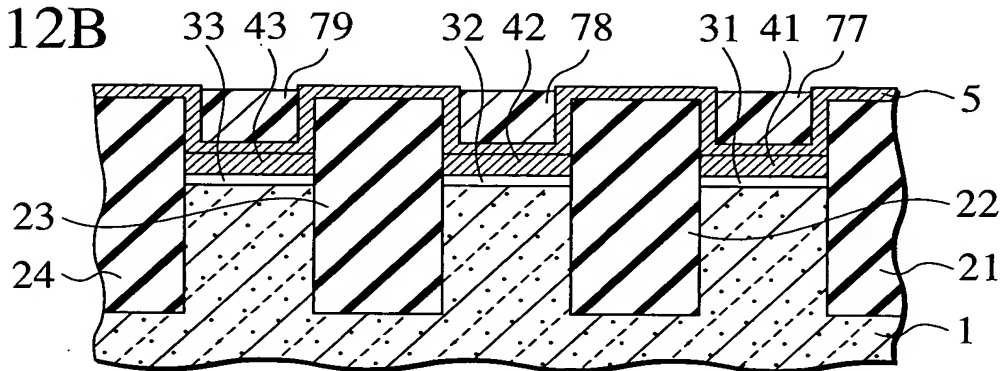


FIG.12C

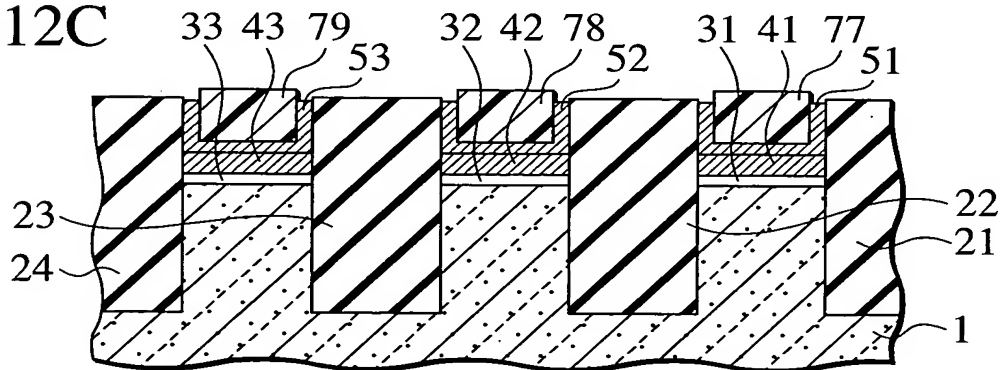
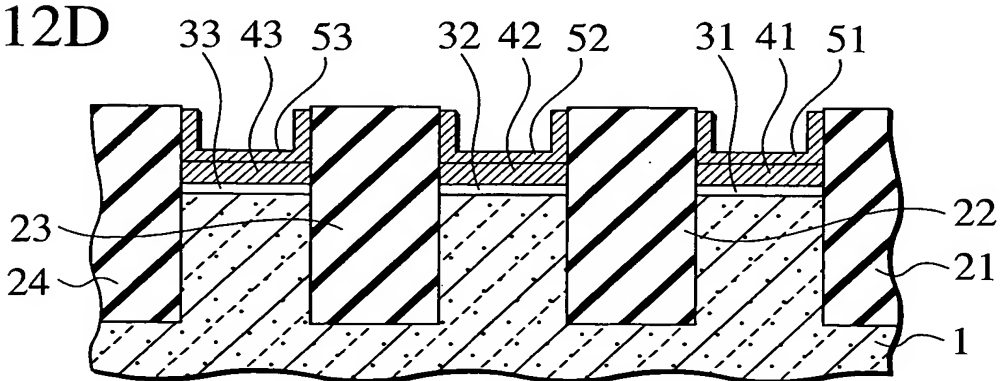


FIG.12D



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FIG.13A

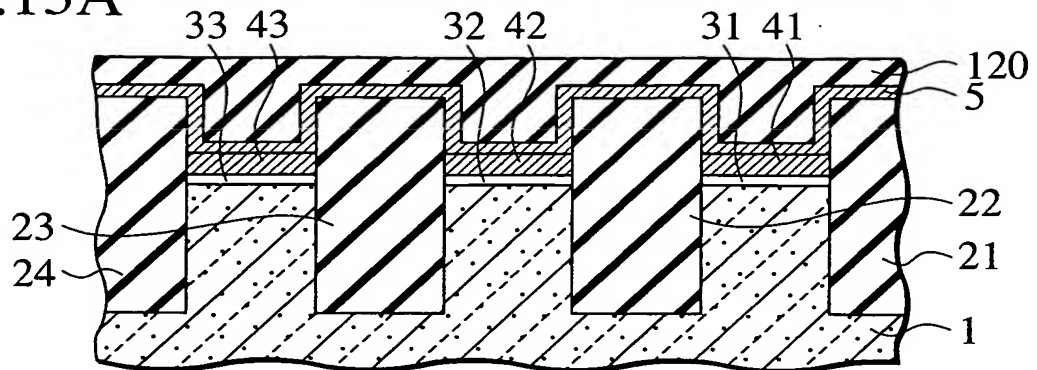


FIG.13B

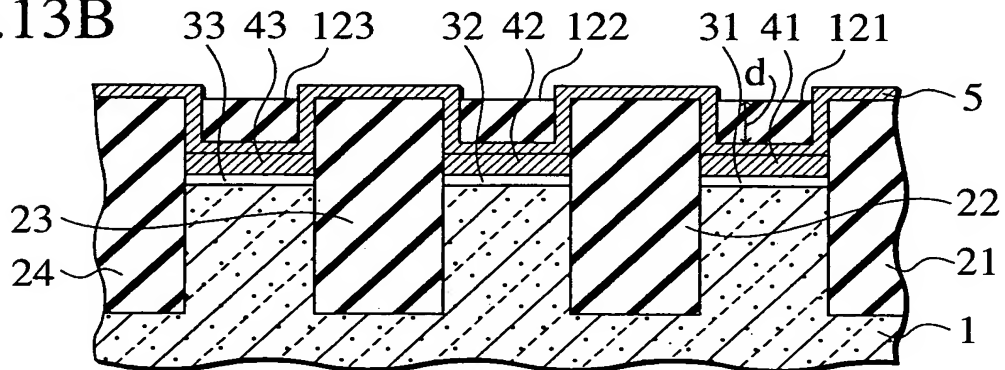


FIG.13C

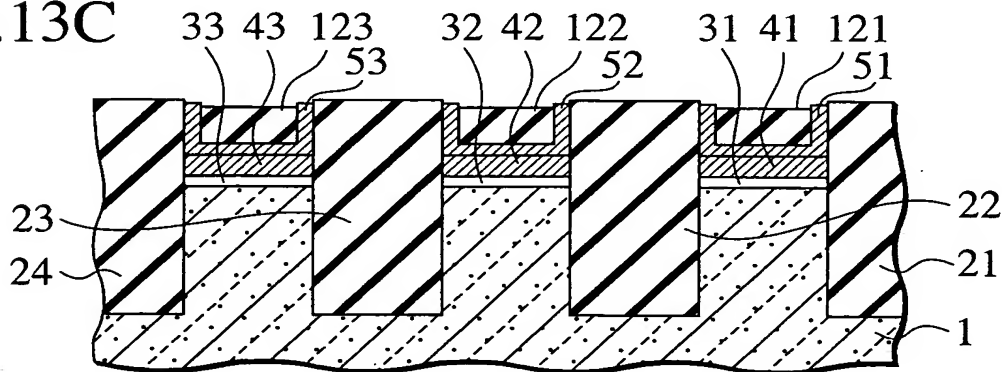
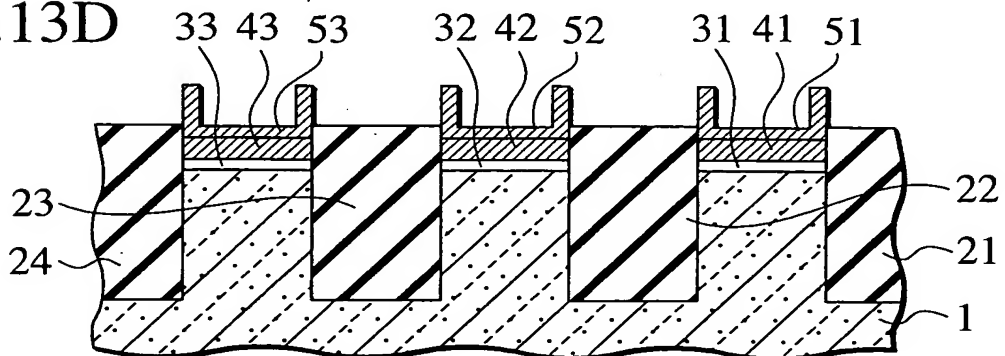


FIG.13D



This cross-sectional view shows three gate structures (79, 78, 77) on a substrate 1. Each gate structure includes a gate stack (33, 32, 31) and a gate spacer (43, 42, 41). The gate stacks are separated by a dielectric layer 59 (for 79), 58 (for 78), and 57 (for 77). The substrate 1 contains regions 126 and 127, and the gate structures are positioned on a layer 124.

FIG.15A

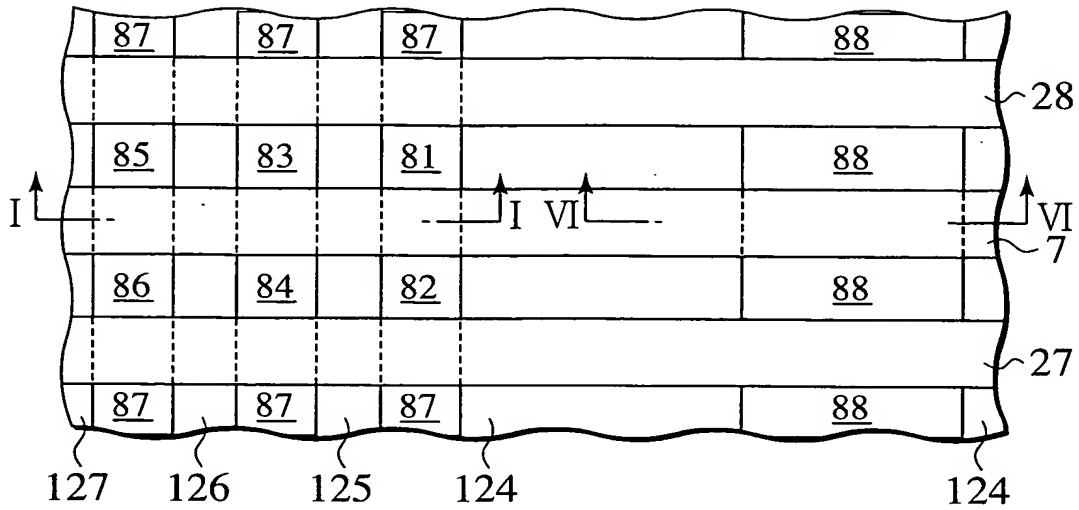


FIG.15B

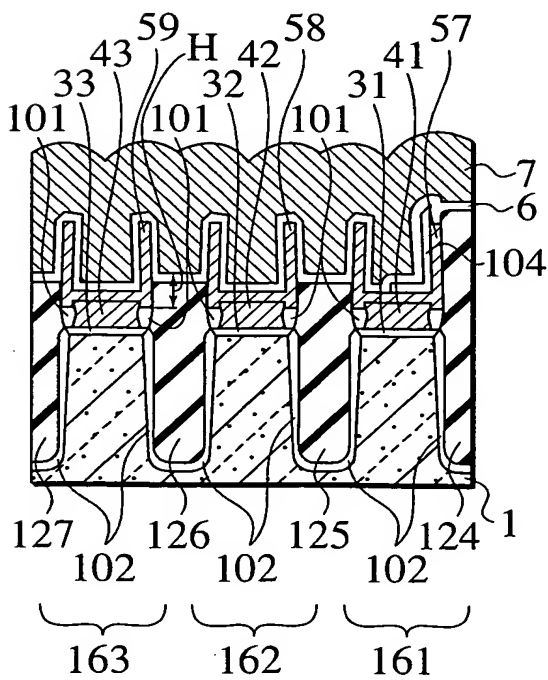
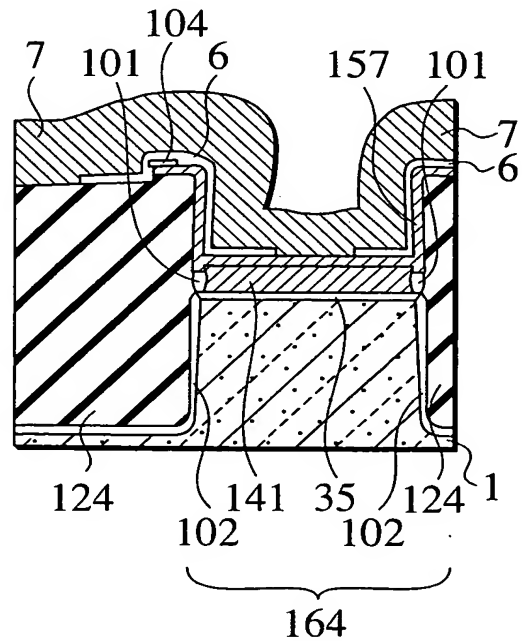


FIG.15C



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FIG.16A

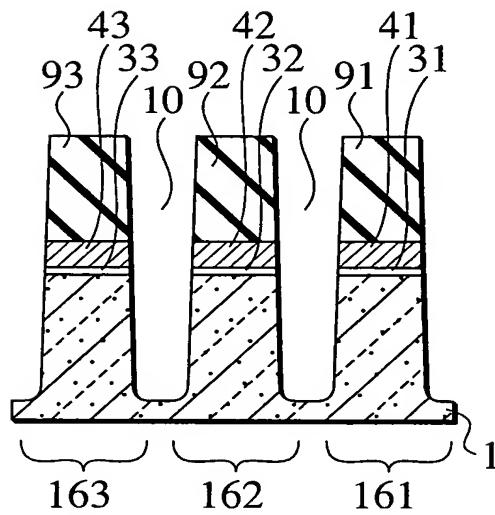


FIG.16B

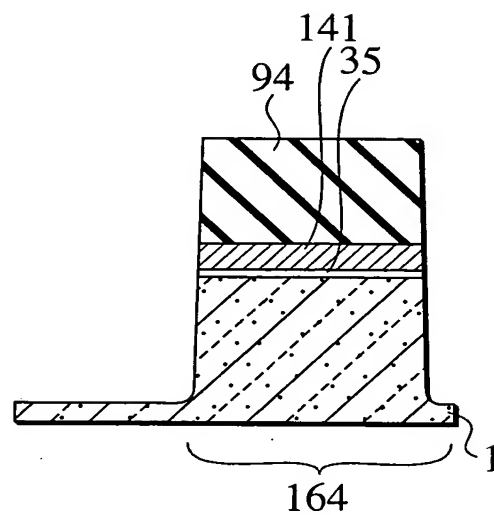


FIG.16C

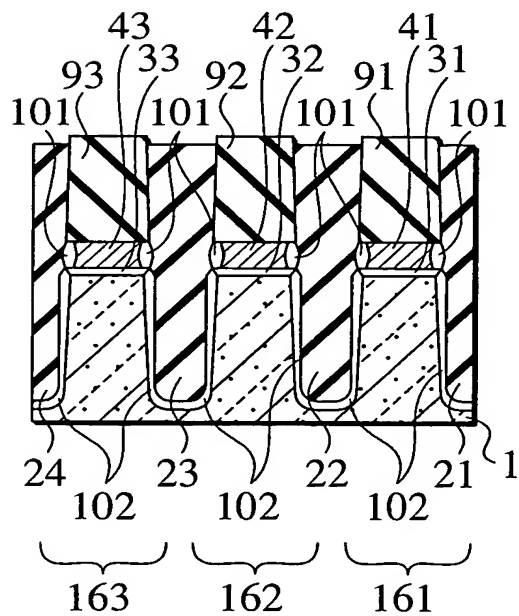


FIG.16D

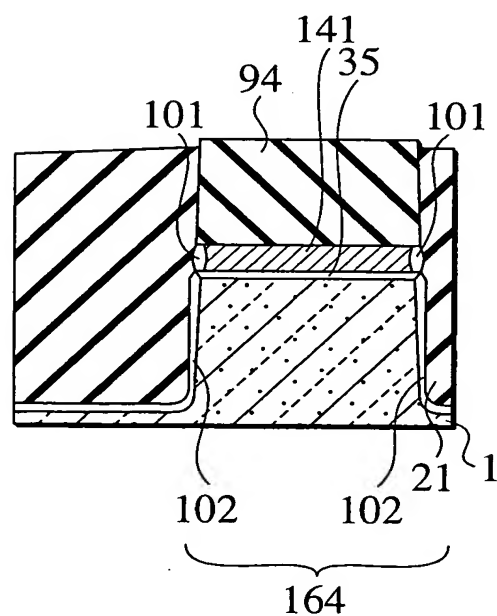


FIG.17A

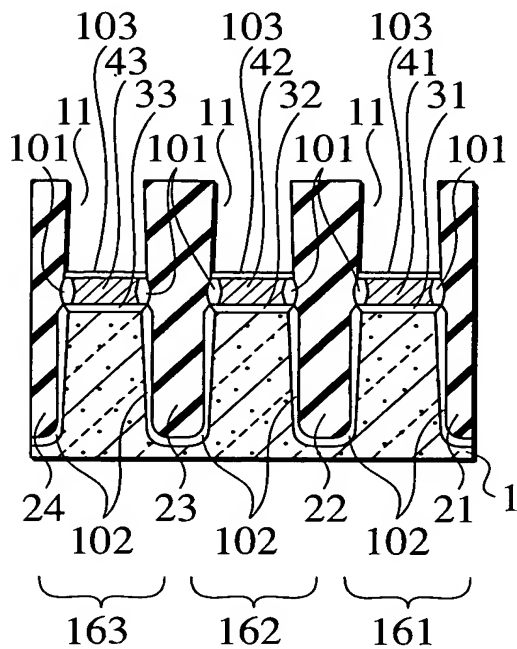


FIG.17B

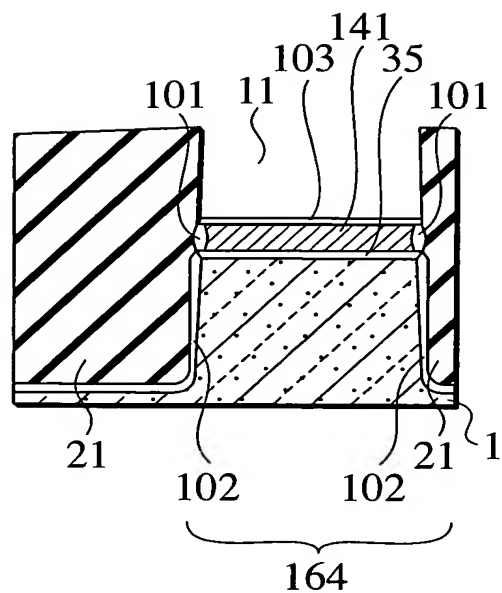


FIG.17C

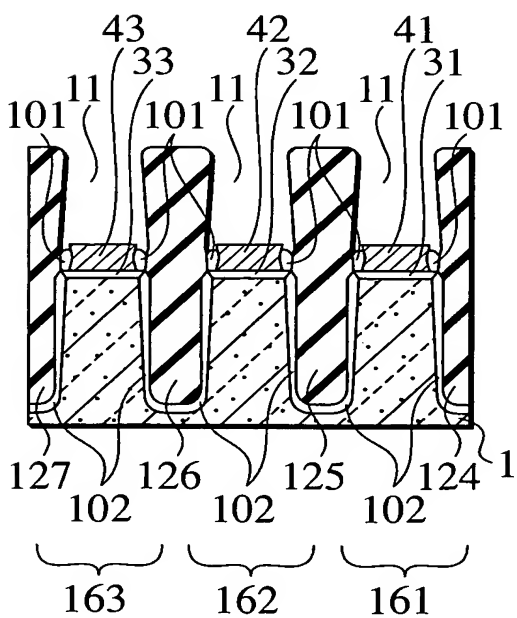


FIG.17D

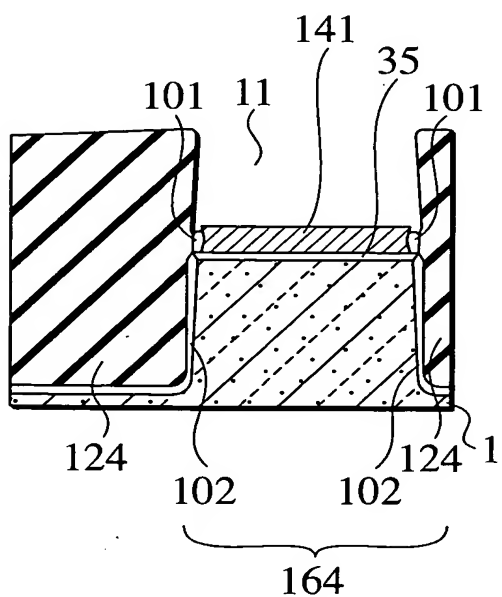




FIG.18A

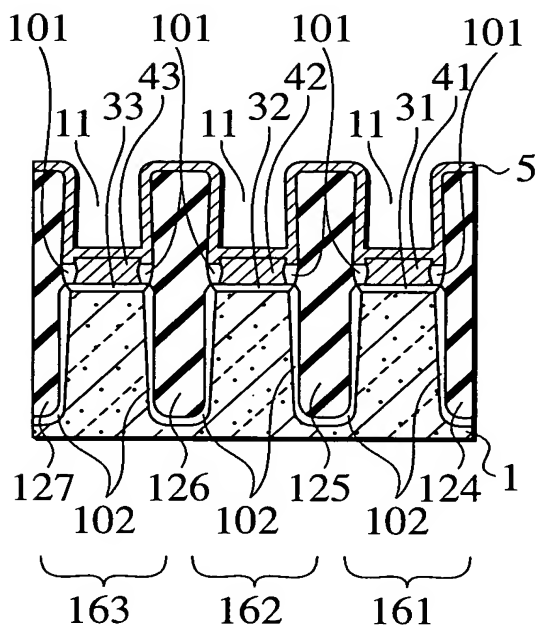


FIG.18B

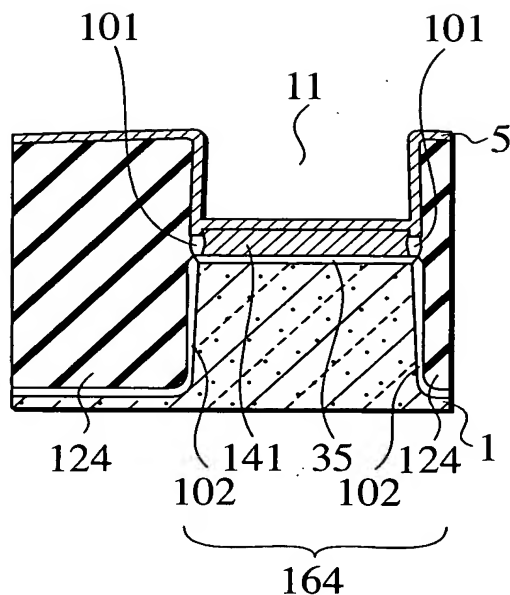


FIG.18C

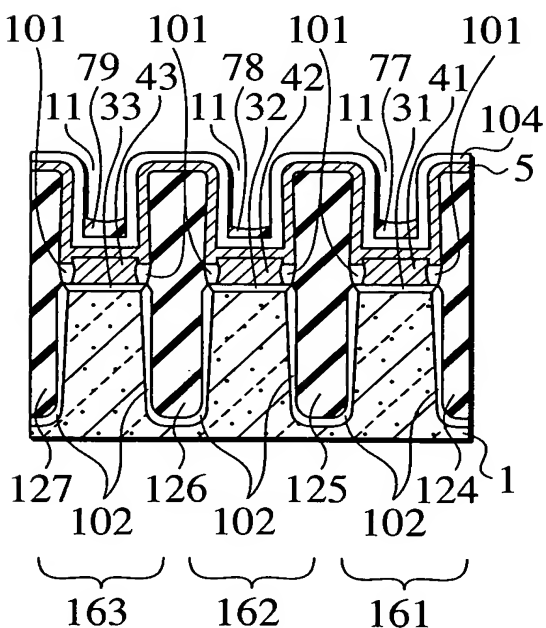
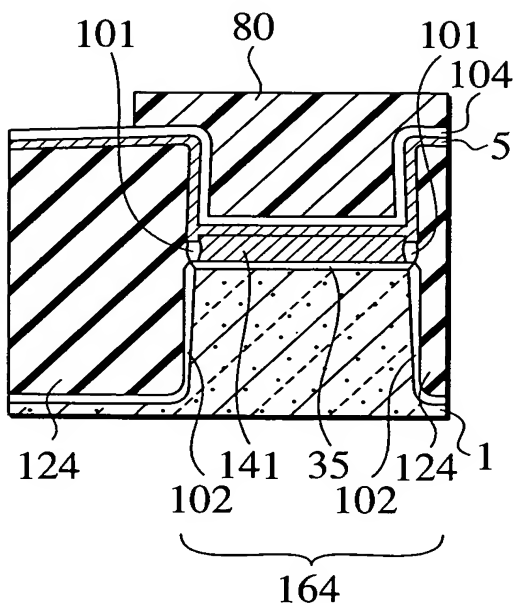


FIG.18D



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FIG.19A

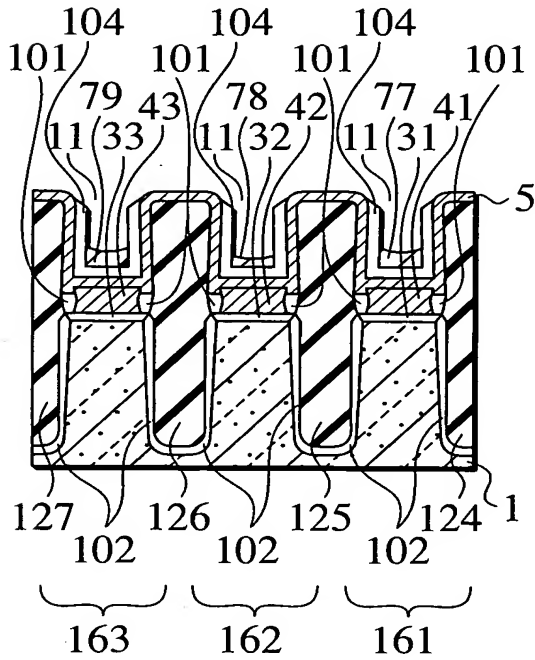


FIG.19B

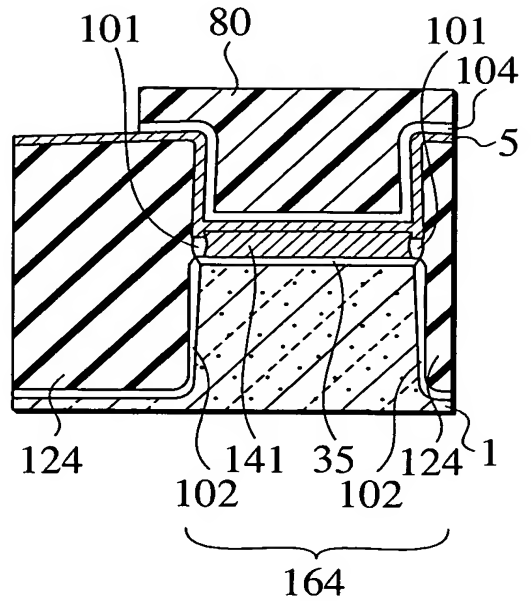


FIG.19C

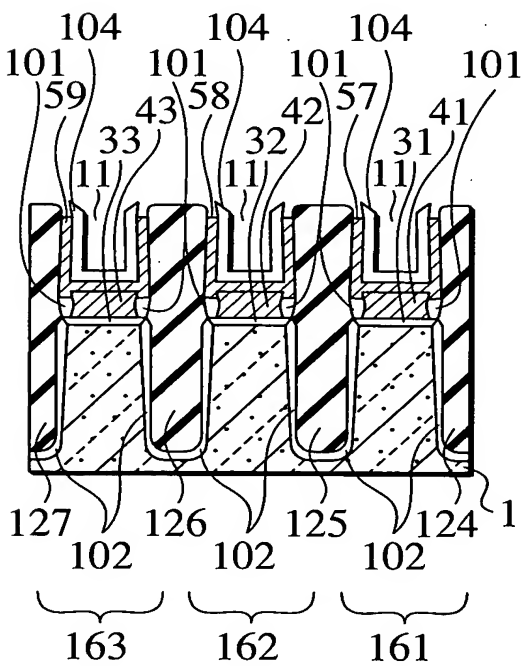
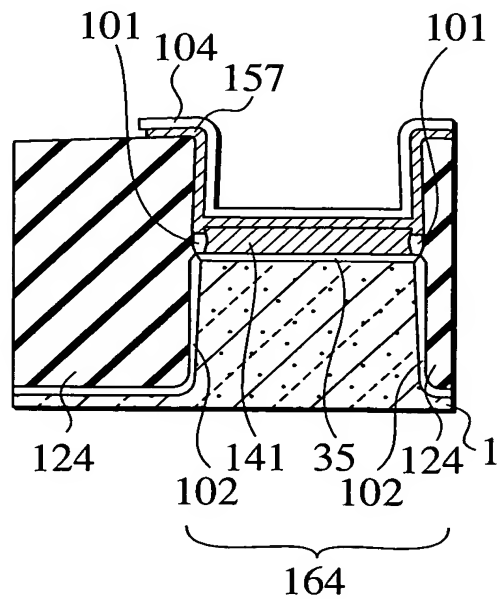


FIG.19D



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FIG.20A

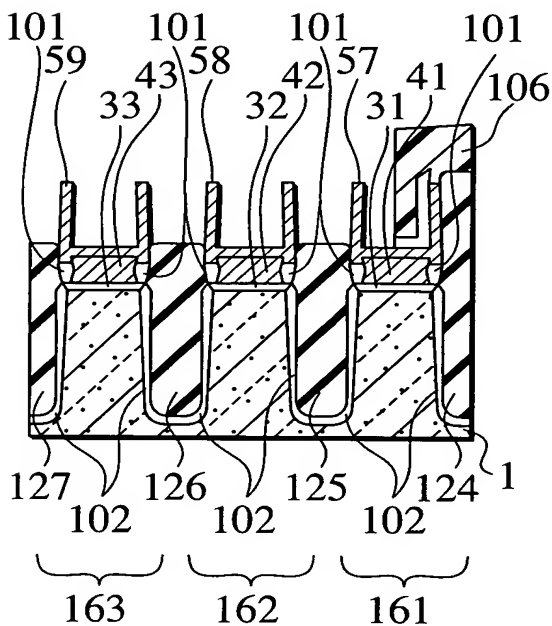


FIG.20B

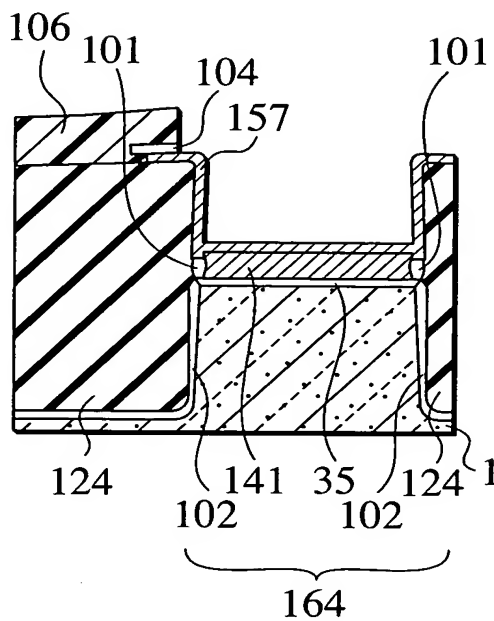


FIG.20C

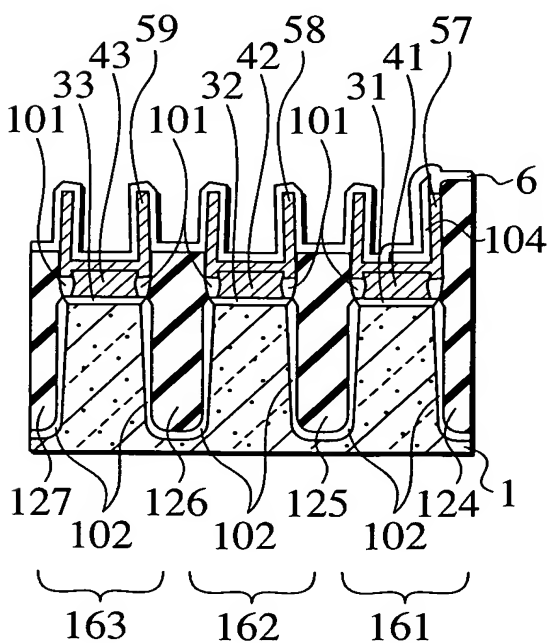
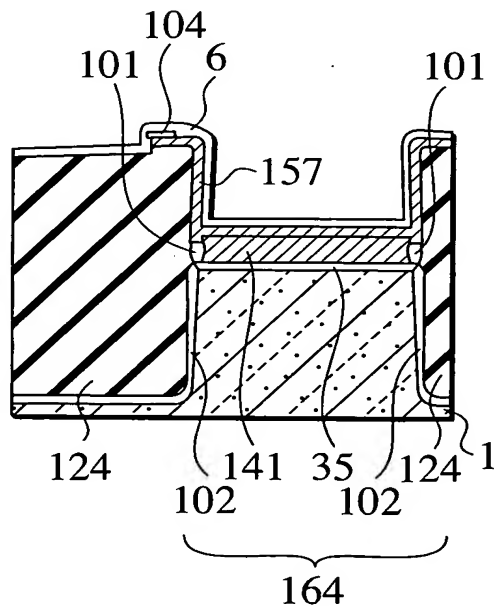


FIG.20D



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FIG.21A

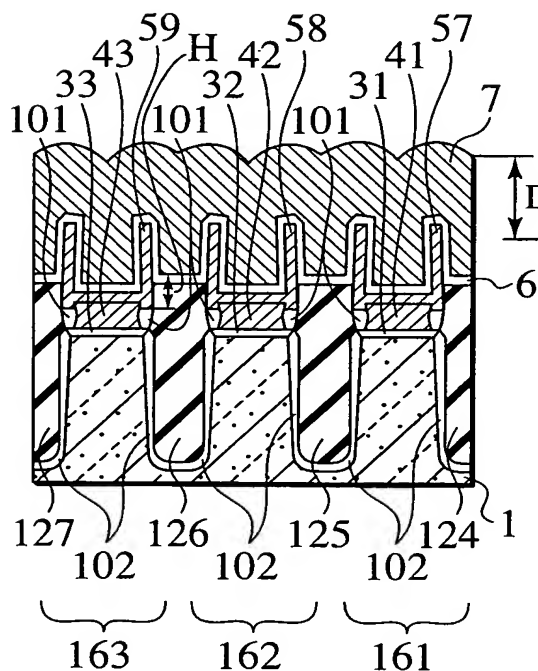


FIG.21B

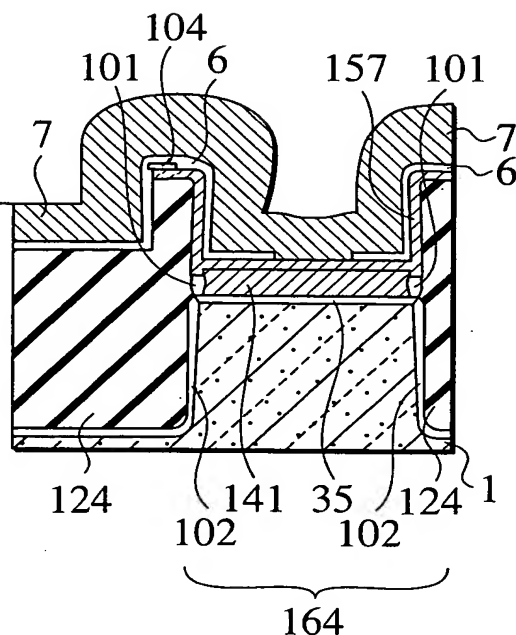
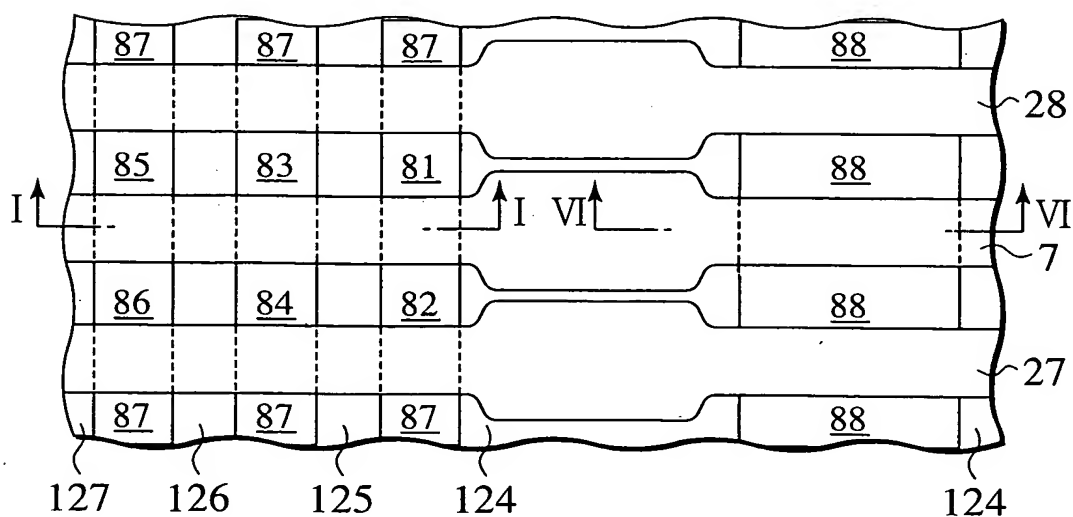


FIG.21C



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FIG.22A

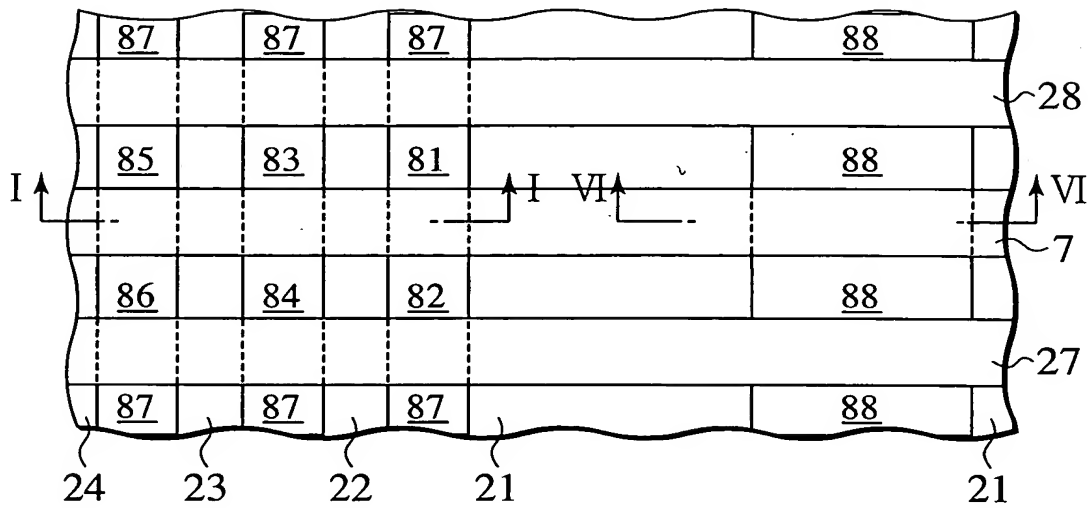


FIG.22B

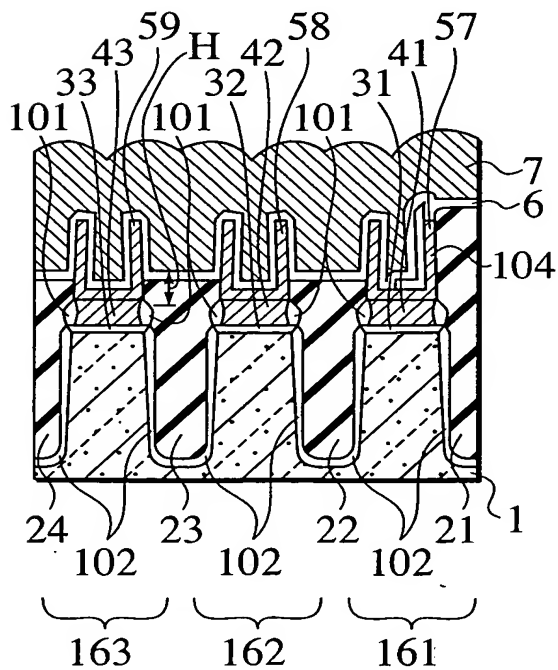


FIG.22C

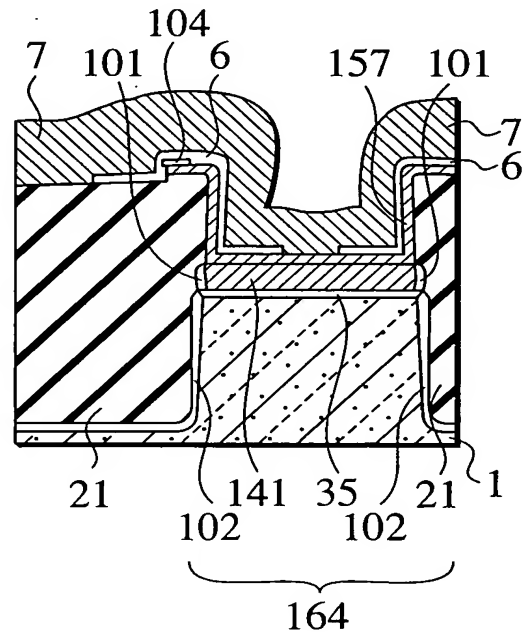


FIG.23A

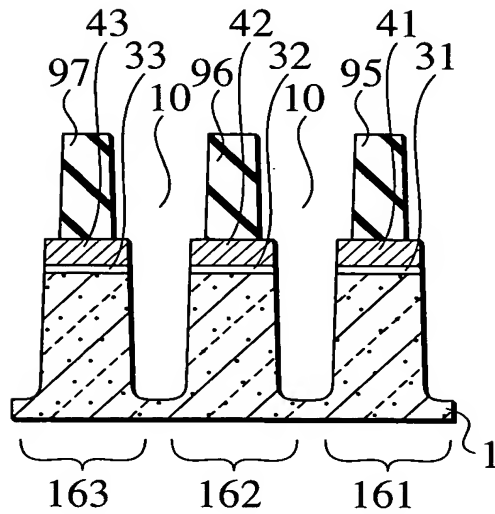


FIG.23B

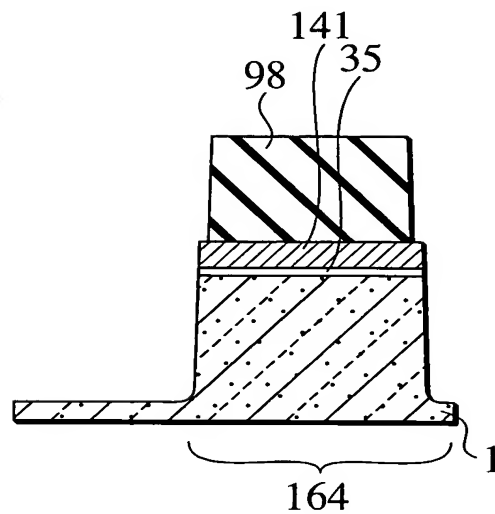


FIG.23C

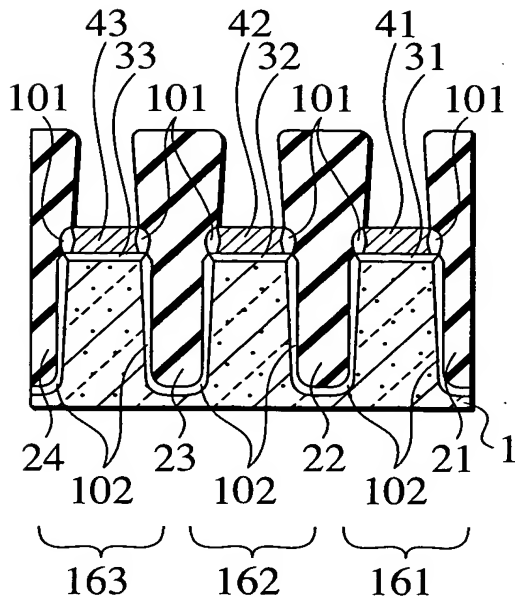


FIG.23D

